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FORM PTO-1449 MODIFTED

Docket No.: 3020.02US02 Application HCEIVED

INFORMATION DISCLOSURE CITATION IN AN APPLICATION

APPLICANT: Meyer

APR 2 2 4002

FILING DATE: July 5, 2001 GROUP ART UNIT: 2123

Technology Center 2100

INITIAL	DOCUMENT	U.S.	PATENT DOCUM	IENTS			
INITIAL							
	NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	,792,913	Dec. 1988	Buckland et al.				
	,985,860	Jan. 1991	Vlach			-	
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

Substitute for form 1449/PTO					Complete if Known							
DE Vo					Application Number	09/899,763						
**NFORMATION DISCLOSURE					Filing Date	July 5, 2001						
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OEMPE.					Examiner Name	Not Assigned						
Sheet	1		of	1	Attorney Docket Number	3020.02US02						
NON PATENT LITERATURE DOCUMENTS												
EXAMINER INITIAL*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published										
		Electronic Circuit Simulation in a Mixed-Language Environment, Litovski et al., Microelectronics Journal, Mackintosh Publications Ltd., Vol. 29, No. 8, August 1, 1998; pgs. 553-558.										
4		Hardware Description Languages for ALECSIS Simulator, Damnjanovic et al., Microelectronics 1995 Proceedings, New York, IEEE, September 1995, pgs. 525-528.										
65,		Verilog-AMS Language Reference Manual, Open Verilog International, December 1999, Los Gatos, California, Chapter 9.										
6		On the Design of Mixed-Mode Simulators for Modern VLSI Circuits, Abdallah et al., Circuits and Systems Proceedings 1995, New York, IEEE, August 1995, pgs. 1168-1171.										
4		A System-Level Simulation Environment for System-on-Chip Design, Schneider et al., 13 <sup>th</sup> Annual IEEE International ASIC/SOC Conference, September 2000, Washington, D.C., pgs. 58-62.										
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